



09/878,442.

cgc

Attorney Docket No. 5308-168

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Das et al.  
Application Serial No.: 09/878,442  
Filed: June 11, 2001  
For: *High Voltage, High Temperature Capacitor and Interconnection Structures*

U.S. Patent No.: 6,972,436 *B2*  
Issued: December 6, 2005

Date: March 16, 2006

Commissioner for Patents  
Attn: Certificate of Correction Branch  
P.O. Box 1450  
Alexandria, VA 22313-1450

**Certificate**  
**MAR 23 2006**  
**of Correction**

**REQUEST FOR ENTRY OF CERTIFICATE OF CORRECTION UNDER  
35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Certificate of Correction be issued. This request is made in order to correct the mistakes incurred through the fault of the U.S. Patent and Trademark Office. No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, with an additional copy thereof and a return post card.

Respectfully submitted,

David C. Hall  
Registration No. 38,904

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**Certificate of Mailing under 37 CFR 1.8**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Attn: Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450 on March 16, 2006.

Monica L. Croom

**MAR 24 2006**

UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTIONPATENT NO. : 6,972,436 *B2*

DATED : December 6, 2005

INVENTOR(S) : Das et al.

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 23.

Line 7 should read -- **24.** The capacitor of claim **23**, wherein the layer of dielectric material is configured to provide a mean time failure of at least about  $10^7$  hours --

MAILING ADDRESS OF SENDER:  
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PATENT NO. 6,972,436  
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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you are required to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

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UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION

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Column 23.

Line 7 should read -- 24. The capacitor of claim 23, wherein the layer of dielectric material is configured to provide a mean time failure of at least about  $10^7$  hours --

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